इलाहाबाद विश्वविद्यालय
UNIVERSITY OF ALLAHABAD
A Central University

Information Brochure
Admission to M. Tech. Program

(Electronics & Communication Engineering/Computer Science & Engineering)

Session 2022-23

Department of Electronics and Communication
(J. K. Institute of Applied Physics & Technology)
University of Allahabad
Prayagraj-211002, India

CONTENTS

I. ADMISSION

1. Guidelines for M. Tech. Application

1.1. Please read the instructions given in the brochure carefully before filling up the application form.

1.2. Application Form & Information Brochure is available on the University website http://www.allduniv.ac.in and on the Department website http://jkinstitute.ac.in/ . Candidates have to download the application form and fill it. Also keep one photocopy of this duly filled form for record.

1.3. Form must be filled in by the candidate. Any false/wrong/incomplete entry in the form will disqualify him from the admission.

1.4. Paste recent colored self-attested photograph in the application form.

1.5. Self-attested photocopies must be attached with the application form as proof of statements filled in for Sr. No. 9 to 13.

1.6. EWS/OBC/SC/ST candidates have to attach respective certificate as per central government rule.

1.7. Candidates claiming weightage for Physically Challenged/NCC 'C' Certificate/Outstanding Sportsman have to attach certificate issued by the Competent Authority.

1.8. Candidates have to attach certificate by the Competent Authority regarding any other special consideration for supernumerary seats (EW/TW/Sponsored by Defense/Government Organization or R & D Lab./Foreign National/Kashmiri Migrant etc.).

1.9. Selected candidate for admission will be informed via email. So, provide a correct email id.

1.10. There is no application fees required with the application form.

1.11. Candidates can submit the application form along with self attested copies of all relevant documents via speed/registered post to the Head, Department of Electronics and Communication, Faculty of Science, MCC Campus, University of Allahabad, Prayagraj-211102, Uttar Pradesh, India. Candidates can also submit the application form by hand to the office of the Department in working hours.

2. Important Dates

Availability of Application form on website: 8th August, 2022 – 23rd August, 2022

Submission of form: 8th August, 2022 – 26th August, 2022 (till 5.00 P.M.)

The candidates selected for admission will be informed through e-mail; the candidates are advised to visit the website regularly for updates.

3. Eligibility

3.1. Eligibility of M. Tech. (Computer Science & Engineering)

3.1.1. An applicant for admission to the academic program leading to the degree of Master of Technology in Computer Science and Engineering of this University shall be considered for admission if-

> 3.1.1.1 a candidate holds the degree of B.Tech. /B.E. in Computer Science and Engineering of this University or any other equivalent degree/or has passed any such examination recognized by this University with an aggregate of at least 50% Marks.

> 3.1.1.2 a candidate holding B.Tech. / B.E. in Information Technology / Electronics Engineering / Electrical Engineering / Electronics and Communication Engineering / Electronics & Instrumentation Engineering **OR** Master's degree in Computer Science/ Computer Applications/ Information Technology or any other equivalent examination recognized by this University with an aggregate of at least 50% Marks and having valid GATE Score in Computer Science and Engineering.

3.1.2. The candidates seeking admission under Rule 3.1.1.1 and 3.1.1.2, and having valid GATE Score (GATE Score shall be valid for a period of next two sessions from the declaration of the results of the GATE Score), a preference shall be given to the candidates having GATE Score over the candidates having no GATE Score.

3.1.3. The candidates without GATE SCORE (NON- GATE) as specified in Rule 3.1.1.1 may be admitted provided that the seat remains vacant even after accommodating all the candidates having valid GATE score as specified in Rule 3.1.1.1 and 3.1.1.2.

3.1.4. There shall be provision for admission of sponsored candidates from Government Organization / R & D Labs and candidates sponsored by Defence Organization. The requirement of GATE shall be relaxed for such candidates.

3.1.5. Merit lists for candidates having valid GATE Score will be prepared for the candidates specified in Rule 3.1.1.1 and Rule 3.1.1.2 by adding 70% of the GATE SCORE and 30% of the marks obtained in the qualifying degree as specified in Rule 3.1.1.1 and Rule 3.1.1.2.

3.1.6. Merit lists for Non-GATE candidate will be prepared on the basis of their marks obtained in the qualifying examination - Discount (if any) + Weightage.

3.2. Eligibility of M. Tech. (Electronics and Communication Engineering)

3.2.1. An applicant for admission to the academic program leading to the degree of Master of Technology in Electronics and Communication Engineering of this University shall be considered for admission if-

> 3.2.1.1. a candidate holds the degree of B. Tech / B. E. in Electronics & Communication Engineering/ Electronics & Telecommunication Engineering of this

University or any other equivalent degree/or has passed any such examination recognized by this University with an aggregate of at least 50% Marks.

3.2.1.2. a candidate holding B. Tech. / B. E. in Electrical Engineering (Electronics Stream) / Computer Science Engineering/ Electronics & Instrumentation Engineering **OR** Master's degree in Computer Science/ Master's degree in Physics (with Electronics as Elective subject) or any other equivalent examination recognized by this University with an aggregate of at least 50% Marks and having valid GATE Score in Electronics & Communication Engineering.

3.2.2. The candidates seeking admission under Rule 3.2.1.1 and 3.2.1.2, and having valid GATE Score (GATE Score shall be valid for a period of next two sessions from the declaration of the results of the GATE Score), a preference shall be given to the candidates having GATE Score over the candidates having no GATE Score.

3.2.3. The candidates without GATE SCORE (NON- GATE) as specified in Rule 3.2.1.1 may be admitted provided that the seat remains vacant even after accommodating all the candidates having valid GATE score as specified in Rule 3.2.1.1 and 3.2.1.2.

3.2.4. There shall be provision for admission of sponsored candidates from Government Organization / R & D Labs and candidates sponsored by Defence Organization. The requirement of GATE shall be relaxed for such candidates.

3.2.5. Merit lists for candidates having valid GATE Score will be prepared separately for the candidates specified in Rule 3.2.1.1 and Rule 3.2.1.2 by adding 70% of the GATE SCORE and 30% of the marks obtained in the qualifying degree as specified in Rule 3.2.1.1 and Rule 3.2.1.2.

3.2.6. Merit lists for Non-GATE candidate will be prepared on the basis of their marks obtained in the qualifying examination - Discount (if any) + Weightage.

4. Number of Seats

4.1 Total no. of seats for admission to the Master of Technology is 18 in each branch.

4.1 (a) The admission of sponsored candidates as specified in Rule 3.1.4 / Rule 3.2.4 will be supernumerary and shall not exceed 5 in total.

4.1 (b) If there are more than 5 candidates in this category then only 5 candidates will be selected on the basis of their merits as per Rule 3.1.2 / Rule 3.2.2 and Rule 3.1.5 / Rule 3.2.5.

5. Reservations

5.1 Reservations to social categories of Scheduled Caste, Scheduled Tribes and Other Backward Classes (OBC), Economically Weaker Section (EWS) and Physically Handicapped (3% Horizontal reservation). Quota to outstanding sports persons will be applicable as per University rules for P.G. admission in the relevant session.

5.2 The admission of sponsored candidates as specified in Rule 3.1.4 / Rule 3.2.4 will be supernumerary and shall not exceed 5 in total. However, there shall be no reservation in case of admission in supernumerary quota.

6. Weightages

6.1 Weightage to NCC 'C' certificate pass candidates and Kashmiri migrants etc. will be given as per University rules for P.G. admission in the relevant session.

6.2 Not more than one weightage shall be admissible to any candidate.

7. Discounts

For admission to M. Tech. Course, preference shall be given to fresh graduates (i.e. candidates who have passed qualifying examination in the year immediately preceding the academic year in which the admission is sought) over the graduates of previous years. A cumulative discount at the rate of 5% of the qualifying examination marks shall be applied for every gap year subject to a maximum discount of 15% in the marks of qualifying examination.

8. Computed Total

The computed total for merit list for each candidate will be compound as follows:

Computed total for candidates specified in Rule 3.1.1.1 / Rule 3.2.1.1 and Rule 3.1.1.2 / Rule 3.2.1.2:

70% of GATE score + 30% of the percentage of marks obtained in the qualifying examination after deductions (the discount percentage if any) + weightage.

Computed total for candidates specified in Rule 3.1.3 / Rule 3.2.3:

Marks obtained in qualifying examination – Discount (if any) + Weightage.

II. GENERAL

9. About Department

Soon after independence, the first Prime Minister of India, Pt. Jawahar Lal Nehru realized the need of a pioneer institute in the fields of Applied Physics, Microwave electronics and Upper Atmospheric physics and other applied physical sciences. On his initiative, an agreement was signed between University of Allahabad and J.K. Trust, Kanpur on November 22, 1947 to establish Department of Applied Physics and Technology in the premises of Muir College Campus, Faculty of Science, University of Allahabad. J. K. Charitable Trust contributed for the construction of the complete building of the institute and hence it was named as J. K. Institute of Applied Physics and Technology. In the year 1976 it was renamed as the Department of Electronics and Communication of University of Allahabad.

The foundation stone of the Institute was laid by the first Prime Minister of IndiaPt. Jawahar Lal Nehru on April 14, 1949. Pt. Nehru also inaugurated the first academic session of the institute on April 4, 1956. Initially a three year M.Sc. (Technology) post-graduate degree course was started in the department. Later in 1967, on the advice of AICTE and UGC, the course was bifurcated into a three year Bachelor of Technology (B.Tech) degree course in Electronics and Telecommunication (after B.Sc.) and to a three semester post graduate degree course i.e. M.Tech. in Electronics Engineering. Further, in the year 1982 a new course/subject in Computer Science (as one of the subjects in B.Sc.) leading to a Bachelor's degree in Science was introduced. Subsequent to the introduction of new subject in Computer Science at undergraduate level, it was desirable to have a full-fledged Post Graduate course in Computer Science. The Department was sanctioned a prestigious DRDO Project (the only centre in North India) by the name "Manpower Development in the area of Computer Science" in the year 1986. Under this project, a Post-graduate course in Computer Science named as M.Sc. Computer Science (Software) was introduced. This project was very closely monitored by DRDO and those who successfully passed out used to get the appointment letter of Sc. "B" along with the M.Sc. Degree. We feel pride in stating that those passed out students today are contributing to the security of the nation. It continued for six batches upto 1992 after which it was taken over as UGC course. In the year 2008, three-year B.Tech. (Electronics and Communication Engineering) course was replaced by 4-year B.Tech. (Electronics and Communication Engineering) degree course after 10+2. A new branch of Engineering in Computer Science leading to Bachelor's Degree in Technology i.e. B. Tech. (Computer Science and Engineering) was also introduced in the year 2008.TheDepartment started a new three-year Post Graduate degree course named as Master of Computer Applications (MCA) from the academic session 2018-19. The institute has a very good placement record and its Alumni of this Department are giving their services in the engineering and computer science fields over in India and abroad and are occupying key positions in their fields.

10. Thrust Areas

Optical Fiber Communication, Microstrip Antenna, Embedded Systems, VLSI technology, Digital Signal Processing, Engineering Materials, Polymer Electronics, Digital Communication, Microwave Engineering, Radar Engineering, Image and Video Processing, Computer Vision, Image Security, Cryptography, High Performance Computing, Wireless Sensor Networks, Data Mining, Machine Learning, Artificial Intelligence, Natural Language Processing, Big Data Analytics, Internet of Things, Cloud Computing, Network Security, Evolutionary Algorithms.

11. M. Tech. Program

Sl. No.	Discipline/Branch	Duration	No. of Seats
1.	Electronics & Communication Engineering	02 years	18
2.	Computer Science & Engineering	02 years	18

12. Fees and Deposits

As per University and Department rules.

13. Financial Support

Scholarship for GATE Qualified candidates as per UGC/MHRD rules.